

**REMARKS/ARGUMENTS**

Various claims are being amended as shown above. The claim amendments clarify the claim language and are not intended to limit the scope of the claims, unless the claim language is expressly quoted in the following remarks to distinguish over the cited art. No new matter is being added by virtue of the amendment to the claims.

In the office action, claim 33 was objected to because of certain informalities. In response to the rejection, claim 33 is being amended to recite a proper antecedent basis for the term "electrical model". For the above reasons, Applicant requests reconsideration and withdrawal of the objection to claim 13.

In the office action, claims 13, 16-20 and 33 were rejected under 35 U.S.C. 102(e) as allegedly being anticipated by Kozak (US Pat. No. 6,414,850). Applicant respectfully traverses the rejection.

Kozak is directed to a main circuit board 114 with a capacitor 412 having capacitance and with a mounted integrated circuit package 112. The capacitor 412 discloses conductive layer 612a that is not disposed between dielectric layers and conductive layer 614c that is not disposed between dielectric layers. Accordingly, claim 13 is distinguishable over Kozak by at least reciting, an assembled substrate wherein each ground plane in the capacitor plate is disposed between a first pair of dielectric layers and each power plane in the capacitor plate is disposed between a second pair of dielectric layers. Accordingly, claim 13 is patentable over Kozak.

Claims 16-20 and 33 depend from claim 13 and are each patentable over Kozak for at least the same reasons that claim 13 is patentable over the cited reference.

Furthermore, each of the claims 16-20 and 33 distinguishes over Kozak by reciting additional features in combination with the features that are recited in their respective base claim. Accordingly, claims 16-20 and 33 are each patentable over Kozak.

For the above reasons, Applicant requests reconsideration and withdrawal of the rejection under 35 U.S.C. §102.

In the office action, claims 14-15 were rejected under 35 U.S.C. 103(a) as allegedly being unpatentable over Kozak or Jodoin in view of Kabadi (U.S. Patent No. 6,097,609), and further in view of Wisser (U.S. Patent No. 3,721,941). Applicant respectfully traverses the rejection.

The Examiner correctly admits in the office action that Kozak does not specifically disclose a first interposer (or socket) between a component and a first electrical contact area on a first side of a substrate and a second interposer (or socket) between a capacitor plate and a second electrical contact area on a second side of the substrate. In an attempt to overcome the deficiencies of Kozak, the Examiner relies on Kabadi and Wisser in an attempt to show various features.

Claims 14-15 each depends from claim 13 and are each patentable over the Kozak-Kabadi-Wisser combination for at least the same reasons that claim 13 is patentable over the cited references, considered singly or in combination. Furthermore, each of the claims 14-15 distinguishes over the Kozak-Kabadi-Wisser combination by reciting additional features in combination with the features that are recited in the respective base claim. Accordingly, claims 14-15 are each patentable over the Kozak-Kabadi-Wisser combination.

For the above reasons, Applicant requests reconsideration and withdrawal of the rejection under 35 U.S.C. §103.

Accordingly, Applicant respectfully requests allowance of all pending claims.


**CONTACT INFORMATION**

If the Examiner has any questions or needs any additional information, the Examiner is invited to telephone the undersigned attorney at (805)681-5078.

Date: August 15, 2007

Respectfully submitted,


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